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OTHER ART - NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	T					
EC.	AA	A. GONZALES M. VALERO, ET AL. "Virtual-Physical Registers", HPCA-4, February 1998				
EC.	AB	C. MOLINA, ET AL., "Reducing Memory Traffic Via Redundant Store Instructions", In Proceedings of the international Conference on High Performance Computing and				
		Networking, April 1999.				
EC	AC	KEVIN M. LEPAK, ET AL. "On the Value Locality of Store Instructions", In Proceedings of the 27th Annual International Symposium on Computer Architecture, June 2000.				
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